



September 12, 2003

TO: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Art Unit 2878 - Examiner Pyo, Kevin K.

FROM: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

RECEIVED
SEP 22 2003
TECHNOLOGY CENTER 2800

SUBJECT: Serial #: 09/896,486
File Date: June 29, 2001
Inventor: Sunetra K. Mendis
Examiner: Pyo, Kvein K.
Art Unit: 2878
Title: An On-Chip Design-for-Testing Structure for CMPS APS
(Acitve Pixel Sensor) Image Sensor


RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

This is in response to the Restriction Requirement in the Office Action dated Aug. 12, 2003. In that office action, restriction was required to one of the following Inventions under

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents P.O. Box 1450, Alexandria, VA 22313-1450 on Sept. 12, 2003.

Signature: 
Stephen B. Ackerman, Reg. No. 37,761

Date: Sept. 12, 2003